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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Stepan Sokolov

Attorney Docket No.: SUN1P842/P6723

Patent: 7,039,904 B2

Issued: May 2, 2006

Title: FRAMEWORKS FOR GENERATION OF  
JAVA MACRO INSTRUCTIONS FOR STORING  
VALUABLES INTO LOCAL VARIABLES

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on June 30, 2006 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

Adrelia M. Sanchez

**REQUEST FOR CERTIFICATE OF CORRECTION  
OF OFFICE MISTAKE  
(35 U.S.C. §254, 37 CFR §1.322)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attn: Certificate of Correction

**Certificate**  
**JUL 11 2006**  
**of Correction**

Dear Sir:

Attached is Form PTO-1050 (Certificate of Correction) at least one copy of which is suitable for printing. The errors together with the exact page and line number where the errors are shown correctly in the application file are as follows:

**CLAIMS:**

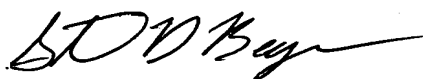
1. In line 29 of claim 1 (column 14, line 15) delete "in". This appears correctly in Amendment C as filed on August 15, 2005, on page 2, paragraph 4, line 1, as claim 35.

JUL 11 2006

Patentee hereby requests expedited issuance of the Certificate of Correction because the error lies with the Office and because the error is clearly disclosed in the records of the Office. As required for expedited issuance, enclosed is documentation that unequivocally supports the patentee's assertion without needing reference to the patent file wrapper.

It is noted that the above-identified errors were printing errors that apparently occurred during the printing process. Accordingly, it is believed that no fees are due in connection with the filing of this Request for Certificate of Correction. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P842).

Respectfully submitted,  
BEYER WEAVER & THOMAS, LLP

A handwritten signature in black ink, appearing to read "SDBeyer", with a long horizontal flourish extending to the right.

Steve D Beyer  
Registration No. 31,234

P.O. Box 70250  
Oakland, CA 94612-0250  
650-961-8300

JUL 11 2006



### **Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of the Claims:**

1-34 (Canceled)

35. (New) A method of storing values in local variables used by a virtual machine, said method comprising:

receiving a first sequence of bytecodes to be executed by said virtual machine, wherein said first sequence of bytecodes is selected from a defined first set of executable virtual machine instructions implemented to conform with a virtual machine specification that includes said defined first set of executable virtual machine instructions;

selecting, at load time, a first-reduced instruction from a reduced set of virtual machine instructions, wherein said first-reduced instruction represents two or more different virtual machine instructions in said first sequence of virtual machine instructions, the virtual machine being arranged to execute the reduced set of virtual machine instructions that provide substantially all of the functionality provided by said defined first set of virtual machine instructions, and wherein every one of the instructions in said defined first set of virtual machine instructions can be represented by at least one of the virtual machine instructions in the reduced-set of virtual machine instructions, and wherein said reduced-set of virtual machine instructions consists of a number of virtual machine instructions which is less than the number executable virtual machine instructions in said defined first set of virtual machine instructions;

translating, at load time, said two or more different virtual machine instructions, in said first sequence of bytecodes, into said first-reduced instruction from said reduced set of virtual machine instructions;

determining, after said translating, a second sequence of bytecodes that includes said first-reduced instruction, thereby representing said first sequence of bytecodes with said second sequence that includes at least said first-reduced instructions from said reduced-set of virtual machine instructions replacing said two or more different virtual machine instructions in said first sequence;

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(Also Form PT-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,039,904 B2

Page 1 of 1

DATED : May 2, 2006

INVENTOR(S) : Stepan Sokolov

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

### In the Claims:

In line 29 of claim 1 (column 14, line 15) delete "in".

MAILING ADDRESS OF SENDER:

PATENT NO. 7,039,904 B2

**Steve D Beyer**  
BEYER WEAVER & THOMAS, LLP  
P.O. Box 70250  
Oakland, CA 94612-0250

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